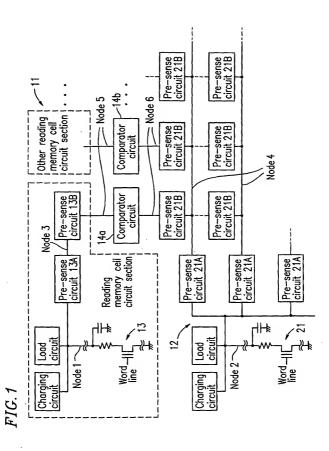
Inventor: Yoshin ORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

Sheet 1 of 14

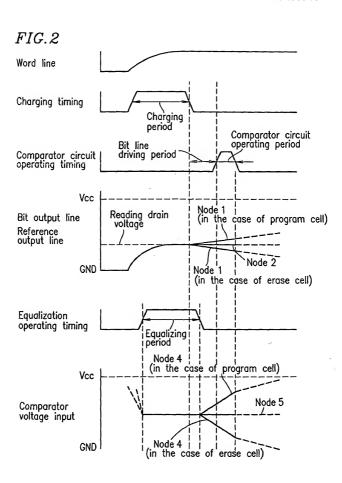
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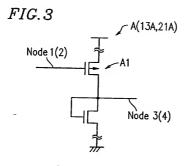
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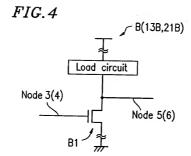




Sheet 3 of 14

01R00719





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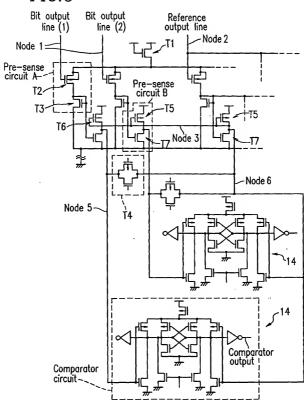
ventor: Yoshinao MORIKAWA Application No.: To Be Assigned

Docket No.: 299002054200

Sheet 4 of 14

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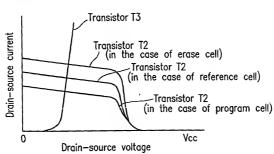
Tide: REFERENCE VOLTAGE GENERATION CIRCUIT FOR SEMICOND OR MEMORY DEVICE,

Inventor: Yos AMORIKAWA
Application No.: To Be Assigned
Docket No.: 299002054200

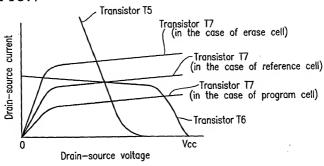
Sheet 5 of 14

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Inventor: nao MORIKAWA
Application No.: To Be Assigned
Docket No.: 299002054200

Sheet 6 of 14

01R00719



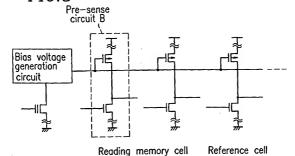
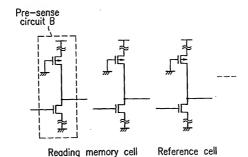


FIG.9

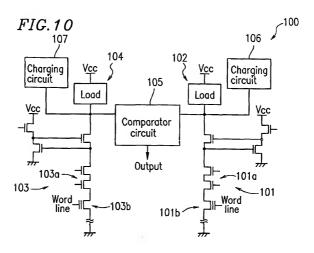


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Title: REFERENCE VOLTAGE GENERATION CIRCUIT FOR SEMICO CTOR MEMORY DEVICE, Inventor, inno MORIKAWA

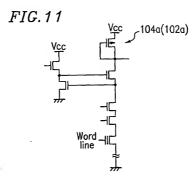
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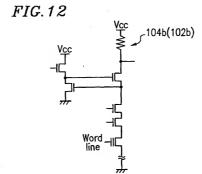
Sheet 7 of 14



Sheet 8 of 14

01R00719



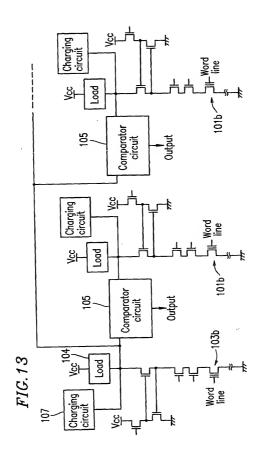


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Application No.: To Be Assigned
Docket No.: 299002054200

Sheet 9 of 14

01R00719

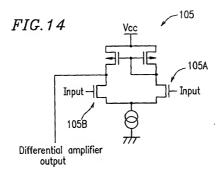


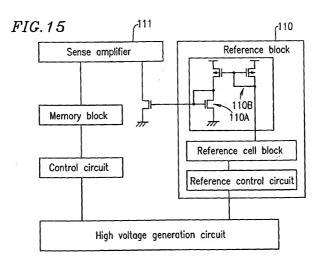
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Title: REFERENCE VOLTAGE GENERATION CIRCUIT FOR SEMICO SUCTOR MEMORY DEVICE, Invento. Invento

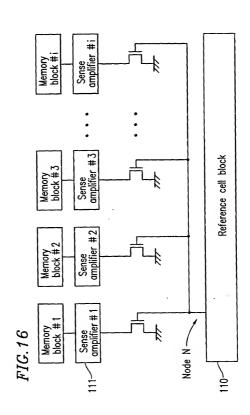
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Sheet 10 of 14



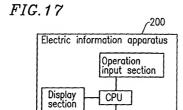


Sheet 11 of 14



Sheet 12 of 14

01R00719



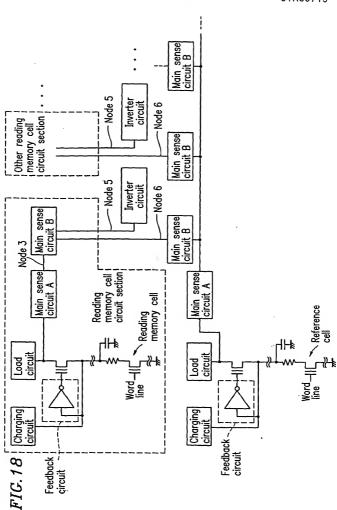
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Inventor: nao MORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

Sheet 13 of 14

01R00719



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Inventor: Yos MORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

Sheet 14 of 14

01R00719

FIG. 19

